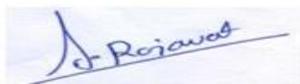


Shri Vaishnav Vidyapeeth Vishwavidyalaya
Bachelor of Technology (Computer and Communication Engineering)
Choice Based Credit System (CBCS)-2018-19

SEMESTER - I

S. No.	COURSE CODE	COURSE NAME	Examination Scheme					Teaching Scheme/Week			CREDITS	TOTAL MARKS
			THEORY			PRACTICAL		Th	T	P		
			End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)					
1	BTMACS101	Mathematics-I	60	20	20	0	0	3	1	0	4	100
2	BTPH101	Applied Physics	60	20	20	30	20	3	1	2	5	150
3	BTCC102	Introduction to Computer and Communication Engineering	60	20	20	0	50	3	1	2	5	150
4	BTEC104	Digital Logic & Circuit Design	60	20	20	30	20	3	1	2	5	150
5	BTCS104	Principles of 'C' Languages	60	20	20	0	0	3	1	0	4	100
6	BTCS108	Programming Skills with 'C'	0	0	0	30	20	0	0	2	1	50
7	BTIT307	Web Development Lab-I(XML & HTML)	0	0	0	60	40	0	0	4	2	100
			300	100	100	150	150	15	5	12	26	800

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.



Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



Joint Registrar
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore